

Listing of Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Previously presented) A device having an interface for controlling radio frequency transceiver circuitry, the interface comprising:

a plurality of connectors for controlling the radio frequency transceiver circuitry including providing control information for changing the mode of operation of the transceiver, said modes including a transmit mode and a receive mode;

a first connector and a second connector, wherein in a first mode, one of said first connector and said second connector supplies data to the transceiver and the other connector of said first connector and said second connector is operable to perform a first function and wherein, in a second mode, one of said first connector and said second connector receives data from said radio frequency module and the other connector of said first connector and said connector is operable to perform a second different function.

2. (Original) A device as claimed in claim 1 wherein said first function is the provision of a first control signal to the transceiver.

3. (Original) A device as claimed in claim 2 wherein the first control signal is a time critical control signal.

4. (Previously presented) A device as claimed in claim 1 wherein the first function is controlling a power amplifier of a transmitter portion of the transceiver.

5. (Previously presented) A device as claimed in claim 1 wherein said second function is the provision of a second control signal to the transceiver.

6. (Original) A device as claimed in claim 5 wherein the second control signal is a time critical control signal.

7. (Previously presented) A device as claimed in claim 1 wherein the second function is controlling dc estimation of data received by a receiving portion of the transceiver.

8. (Previously presented) A device as claimed in claim 1 wherein said second function is reception of data from the transceiver.

9. (Previously presented) A device as claimed in claim 1 wherein the first connector is bi-directional and supplies data in the first mode and receives data in the second mode.

10. (Previously presented) A device as claimed in claim 1 wherein predetermined time critical control signals are not provided via said plurality of connectors.

11. (Previously presented) A device as claimed in claim 1 wherein the first mode is the transmit mode for the transceiver.

12. (Previously presented) A device as claimed in claim 1 wherein the second mode is the receive mode of the transceiver.

13. (Previously presented) A device as claimed in claim 1 wherein the plurality of connectors includes a connector for transferring data to and from the device, a connector for providing an enable signal from the device and a connector for providing a clock signal from the device.

14. (Previously presented) A device as claimed in claim 1 wherein the plurality of connectors are used to read from and write to registers in the transceiver.

15. (Previously presented) A device as claimed in claim 1 wherein the plurality of connectors are a serial interface having at least one connector via which data is transmitted serially, said data including a device address, a bit indicating whether data is for writing or is to be read, a local address and a variable data portion.

16. (Previously presented) A device as claimed in claim 1, further comprising first control circuitry and a processor, wherein the first control circuitry is arranged to control the radio frequency circuitry via the plurality of connectors and/or the first and second further

connectors and the processor is arranged to control the radio frequency circuitry only via the plurality of connectors.

17. (Previously presented) A device as claimed in claim 15 wherein the data portion varies between 1 and 32 bits.

18. (Previously presented) A device as claimed in claim 1 wherein the plurality of connectors are coupled to at least one other device.

19. (Previously presented) A device as claimed in claim 1 comprising a connector for receiving a clock signal from the transceiver.

20. (Previously presented) A device as claimed in claim 1 comprising a third connector for powering down components of the transceiver.

21. (Previously presented) Transceiver circuitry having an interface for connection to a device having baseband circuitry, the interface comprising:

a plurality of connectors for providing control information for changing the mode of operation of the transceiver, said modes including a transmit mode and a receive mode;
a first connector and a second connector, wherein in a first mode, data is received at one of said first connector and said second connector and the other connector of said first connector and said second connector performs a first function and wherein, in a second mode, data is

provided at one of said first connector and said second connector for transfer to the device and the other connector of said first connector and said second connector performs a second function different from the first function.

22. (Previously Presented) Transceiver circuitry as claimed in claim 21 comprising a power amplifier, wherein the first function is the reception of a first control signal for controlling the power amplifier.

23. (Previously Presented) Transceiver circuitry as claimed in claim 21 comprising DC estimation circuitry wherein the second function is the reception of a second different control signal for controlling the dc estimation circuitry.

24. (Original) Transceiver circuitry as claimed in claim 21 or 22 wherein the second function is the provision of received data.

25. (Original) A method of interfacing a device having baseband circuitry to transceiver circuitry, the device having means for controlling whether the transceiver is in a transmitting mode or a receiving mode and first and second connectors, comprising the steps of:
controlling the transceiver to enter the transmitting mode;
providing data from the device to the transceiver via the first connector; and
controlling the power amplifier in the transceiver via the second connector.

26. (Previously presented) A method of interfacing a device having a baseband circuitry to a transceiver, the device having means for controlling whether the transceiver is in a transmitting mode or a receiving mode and a first connector and a second connector, comprising the steps of:

controlling the transceiver to enter the receiving mode;
receiving data at the device from the transceiver via the first connector; and
controlling the dc estimation in the transceiver via the second connector.